

AVX-SIP SERIES

SINE WAVE TO TTL PULSE CONVERTERS **AVX-STR SERIES**

SUB-NANOSECOND PULSE STRETCHERS

AVX-SIP SERIES

The AVX-SIP series is useful in experimental applications where a sine wave signal must be converted to logic-level pulses, to trigger other equipment.

The AVX-SIPA-PS converts a sine-wave input with an amplitude in the range of 0.2V to 5V peak-to-peak to TTL levels (0 and +3 to +5V). The input frequency may be as high as 50 MHz. The output pulse width is adjustable from 10 ns to 10 us using a three-position decade range switch and a one-turn vernier control.

The AVX-SIPB-PS is similar, except that the output pulse width is fixed at 50% of the input period. In other words, the output duty cycle is 50% (for a true sine wave input).

The high-speed AVX-SIPC-PS operates up to 250 MHz. The output logic levels are ECL (-1.6V and -0.8V), for compatibility with high-speed logic circuitry.

All models require 100 - 240 Volts AC, 50 - 60 Hz prime power and are equipped with BNC input and output connectors. All models are also available in a wider, rack-mountable version.

| Madali | | | |
|---------------------------|---|--------------------|--|
| Model: | AVX-SIPA-PS | AVX-SIPB-PS | AVX-SIPC-PS |
| Input frequency: | 0 - 50 MHz 0 - 250 MH | | 0 - 250 MHz |
| Input amplitude: | 0.2 to 5 Volts, peak-to-peak | | |
| Input resistance: | 50 Ohms | | |
| Output amplitude: | TTL levels: Low: 0V High: +3 to +5V | | ECL levels: Low: -1.6V High: -0.8V |
| Output pulse width: | 10ns - 10us, adjustable | One-h input per | nalf of iod, fixed |
| Output duty cycle: | 50%, maximum | | %, ed |
| Connectors: | BNC | | |
| Power requirement: | 100 - 240 Volts, 50 - 60 Hz | | |
| Dimensions ¹ : | 100 x 215 x 375 mm (3.9" x 8.5" x 14.8") | | |

AVX-STR SERIES

The AVX-STR series is useful in experimental applications where an ultra-fast low-level pulse must be converted to standard logic levels and widened, to trigger other equipment.

The AVX-STRA-PS converts pulses with widths of 200 ps or higher, and amplitudes of 30 mV to 1V, to TTL levels (0 and +3 to +5V). The output pulse width is fixed at 50 ns, suitable for triggering most laboratory instruments. The input pulse repetition frequency may be as high as 10 MHz.

The AVX-STRB-PS handles slower, larger input pulses. This model converts pulses with widths of 2 ns or higher, and amplitudes of 300 mV to 3V, to TTL levels (0 and +3 to +5V). The output pulse width is fixed at 50 ns, suitable for triggering most laboratory instruments. The input pulse repetition frequency may be as high as 10 MHz.

All models require 100 - 240 Volts AC, 50 - 60 Hz prime power and are equipped with SMA input and output connectors. All models are also available in a wider, rack-mountable version.

| Model: | AVX-STRA-PS | AVX-STRB-PS | | |
|---|---|-------------------|--|--|
| Input pulse width: | > 200 ps | > 2 ns | | |
| Input amplitude: | +30 mV to +1.0 V | +300 mV to +3.0 V | | |
| Input resistance: | 50 Ohms | | | |
| Input pulse repetition frequency: | 0 to 10 MHz | | | |
| Output amplitude: | TTL levels: Low: 0V High: +3 to +5V | | | |
| Output pulse width: | 50 ns, fixed | | | |
| Propagation delay: | < 20 ns | | | |
| Connectors: | SMA | | | |
| Power requirement: | 100 - 240 Volts, 50 - 60 Hz | | | |
| Dimensions ¹ : | 100 x 215 x 375 mm (3.9" x 8.5" x 14.8") | | | |
| I) Add -R5 option to specify wider rack-mountable chassis (100 x 430 x 375 mm, 3.9" x | | | | |

 Add -R5 option to specify wider rack-mountable chassis (100 x 430 x 375 mm, 3.9 17" x 14.8") with rack-mount kit.





AVX-SIPA-PS

AVX-STRA-PS

Avtech also offers frequency dividers, delay generators, and burst generators. Visit http://www.avtechpulse.com/select/ for these products!